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48154 7590 03/08/2005

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Kristin R. Hayes	(Depositor's name)
<i>K. Hayes</i>	(Signature)
June 3, 2005	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/785,140	02/24/2004	Siva Chemupati	2003 P 50641 US	8178

TITLE OF INVENTION: CIRCUIT MODULE HAVING INTERLEAVED GROUPS OF CIRCUIT CHIPS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	06/08/2005
EXAMINER	ART UNIT	CLASS-SUBCLASS			
NGO, NGAN V	2818	257-686000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

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Infineon Technologies AG

Munich, Germany

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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FACSIMILE COVER SHEET

To Commissioner for Patents	Total Pages Sent: 3
Mail Stop Issue Fee	(including cover sheet)
Facsimile Number: (703) 746-4000	Transmission Date: June 3, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chennupati	Docket No.: 2003 P 50641 US
Serial No: 10/785,140	Art Unit: 2818
Date Filed: February 24, 2004	
Title: Circuit Module having Interleaved Groups of Circuit Chips	

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at (703) 746-4000 on the date shown above:

- Certification of Facsimile Transmission (1 page)
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Respectfully submitted,


Kristin R. Hayes
Legal Assistant

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